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Computer Basics

... **machines** can understand, whereas a **high-level** programming language is ... not make him busy with **translating** the Java ... packages are then divided **into** many categories ...

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... 10. **High-Level** Language. **Level** 5. ... 15. Digital Logic. **Level** 0. CPU, constructed from digital logic gates. ... Binary Numbers. **Translating** between binary and decimal. ...

bsp.csie.nctu.edu.tw/courses/asm-sp/slide/alsp01.ppt - [Similar pages](#)

CommsDesign - NPU software taps virtual machine approach

... one could devise an extremely **high-level** functional (as ... or session, removing an outer header, **translating** IP addresses ... written in the language **into** the **virtual** ...

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... Commands Type Interactors Java low **high high** Figure 3 ... processing, Java is at the **level** that Postscript is in ... **Translating** agent rules directly **into** Java bytecode ...

www.cs.colorado.edu/~l3d/systems/agentsheets/Documentation/VL-97-Paper.pdf -

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The Jikes Research Virtual Machine (RVM)

... The optimizing compiler begins by **translating** Java bytecodes to ... Low-level optimization After **high-level** analyses and ... to a low-level intermediate representation ...

www-106.ibm.com/developerworks/java/library/j-jalapeno/ - 101k - [Cached](#) - [Similar pages](#)

ITworld.com - Compilers, Interpreters and Bytecode

... With bytecode, a **high-level** program is translated **into** an ... a JIT compiler translates code **into** the host ... aims to boost performance by **translating** the most ...

www.itworld.com/AppDev/400/CWD012201Compilers/ - 44k - Sep 6, 2004 -

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7 VMPP: a virtual machine for parallel processing*Loyot, E.C., Jr.; Grimshaw, A.S.;*

Parallel Processing Symposium, 1993., Proceedings of Seventh International , 13-16 April 1993

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5 Design of a Machine-Independent Optimizing System for Emulator Development

Perng-Ti Ma, T. G. Lewis

April 1980 **ACM Transactions on Programming Languages and Systems (TOPLAS)**, Volume 2 Issue 2


Full text available:  [pdf\(1.32 MB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

Methods are described to translate a certain machine-independent intermediate language (IML) to efficient microprograms for a class of horizontal microprogrammable machines. The IML is compiled directly from a high-level microprogramming language used to implement a virtual instruction set processor as a microprogram. The primary objective of the IML-to-host machine interface design is to facilitate language portability. Transportability is accomplished by use of a field descript ...

Machine-adaptable dynamic binary translation

David Ung, Cristina Cifuentes

January 2000 **ACM SIGPLAN Notices , Proceedings of the ACM SIGPLAN
workshop on Dynamic and adaptive compilation and optimization,**
Volume 35 Issue 7

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Dynamic binary translation is the process of translating and optimizing executable code for one machine to another at runtime, while the program is "executing" on the target machine.

Dynamic translation techniques have normally been limited to two particular machines; a competitor's machine and the hardware manufacturer's machine. This research provides for a more general framework for dynamic translations, by providing a framework based on specifications of machines that ...

Keywords: binary translation, dynamic compilation, dynamic execution, emulation, interpretation